# Research Article

# Microcontroller based Hybrid Power Electronics Trainer using PWM Techniques

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#### Abstract

The project is focused on the main concepts of the power electronics field and the related application of some major components studied in these courses. Initially, different circuits will be designed according to given experiments list and they will combine up to make a trainer together. The Proteus software will do the designing. Last but not the least the main factor, which will be making our project trainer different from the other will be the circuity of the micro controller through which we will be controlling some angle delays and the timing factor of Power Electronic IC's circuit with software implementations. In the future, we will be watching our trainer in different universities. We hope that students should get better knowledge about these labs through our trainer then the present ones.

Keywords: VFD; SPWM; Buck; Boost; Dimmer

#### Introduction

In the present world, the person with the practical knowledge is more valuable than the one who reads book and ratify the concepts instead of implementing those in the labs. Our project is highlighting the course of power electronics and at the present condition of the lab; we tried to develop a trainer with some important concepts of the course and highlighted experiments, which are not being considered much important at the present. The innovation, which is provoked in our trainer that makes it best then the other trainers, will be its design and the control of SCR through the micro controller. Some of the real life applications are also a part of our trainer which will motivate our future engineers to make something on their know something better than the world has before implemented. The main difference between our trainer and the present lab-volts trainers in the lab is the cost, the design of the circuits, space problem and last but not the least the participation of the student in the experiment performed on the trainer

#### **Scope of the Project**

1) The cost of the project is effective and it is the main factor which is differentiating it from other trainers

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- 2) Understanding of the circuitry and components Will be easy on this trainer because of its design. All Circuitry will be viable to students.
- 3) The circuit Will be printed on them as they are been made on the textbook.
- 4) Advanced power electronics circuit typologies and controls: from intelligent gate drives for MOSFETs and IGBTs to new converter/inverter circuitry.
- 5) Micro Controller is used to control the angle delays of Power Electronic IC's.

#### Methodology

- 1) Circuit designing on software (PROTEOUS, MATLAB)
- 2) Code implementation (IDE)
- 3) Software simulations
- 4) Implementation of circuits of PCB boards
- 5) Comparison of results with the software.
- 6) Trainer structure drawing (COREL DRAW)
- 7) Circuits attachments to the designed structure.

# **System Description**

This trainer is designed to highlight the different circuits of the power electronics. The trainer is comprising of 10 different circuits with their practical implementations. For sure the supply to the circuits is given internally but for demonstration we have included knobs on the top surface for best guidance of the students. We are controlling the timing factor of the power electronic IC's and some waveform through the use of microcontroller. For this purpose, we also need to compensate with the price of the trainer so that it could be cheap then the present trainers. For that we are using the Arduino-Nano Micro-controller. In this trainer we have focused the designs of the circuits and for that we have write the compatible codes for the present designs of the trainer.



Figure 1: Block Diagram

Below is the experiment list, which are performed on this trainer.

- 1) Diac and Triac characteristics experiment
- 2) Buck Regulator
- 3) Boost Regulator
- 4) SPWM Invertor
- 5) IGBT characteristic and motor speed control experiment
- 6) MOSFET characteristic and motor speed control experiment
- 7) Single Phase Variable Frequency Drive
- 8) Single-Phase thyristor converter with resistive load
- 9) Single-phase thyristor Full converter

# Single phase variable frequency drive (VFD)

Inductive loads like motors are not happy at operating with the frequencies that are not within their manufacturing specifications and show signs of inefficiency if compelled to operate under such undesirable conditions. For example, a motor specified to operate at 60 Hz will show abnormal behavior if subjected to operating frequency of any other frequency.



Figure 2: VFD Block Diagram

Heating up of motor, inferior or sophisticated than the required speed and high ingesting of power with reduction in life span are the outcomes of operating inductive loads at unspecified frequency.

#### Spwm inverter

Inverter is a device, which converts a DC voltage into an AC voltage. The input and output voltage and frequency all depend on the designing of the circuit. Inverter power supply makes use of power electronics technology. Numerous fields such as power, electronics and microcontroller are int its span. It is one of the host spots of today's industry and numerous techniques have taken into account for its designing.



Figure 3: SPWM inverter Block Diagram

#### **DC-DC converters**

The transformation of the Electric power from one arrangement to another requires need of Power Electronics and switching physiognomies of Power electronics devices. The Static Power Converters helps us in achieving our areas. The industry of power electronics has progressed from past few decades along with the necessities like increased battery life, brighter and full HD displays and increased talk time on phones. Power consumption has been utmost requirement of the industry. To oblige with the growing demands engineers have developed efficient conversion circuits which have resulted in the growth of interdisciplinary field of Power Electronics

**Buck Converter:** Just like step down AC transformer, Buck Converter is a step-down dc-dc converter. It includes two switches (transistor and diode) for directing current across inductor. The inductor gains energy from load when switch is on and then discharges it across load when the switch opens.



Figure 4: Basic Diagram

**Boost Regulator:** A step-up converter steps up input DC-voltage and provides at output. The components used here are almost same as that of buck regulator, yet in different arrangements



Figure 5: Basic Diagram

# **Motor Speed Control using 555**

From past decades, numerous ways have been applied for controlling speed of a DC MOTOR. We control the motor by varying input voltage of a motor. In this regard, we can apply PWM method for controlling the motor.



Figure 6: Astable Circuit

# **TRIAC Characteristics Experiment**

Our light sources such as tube lights have their power rating. If high brightness is required, high watt bulb would have to play its role and vice versa. Instead of changing the light bulb, light dimmer circuits are designed in order to control the brightness of a bulb. This is done through a switch known as TRIAC. Several methods are opted in order to control this device with its prongs and cons.



Figure 7: Block Diagram of Light Dimmer

# **Controlled Rectifiers**

In power electronics, rectifiers are characterized into controlled rectifiers and un-controlled rectifiers. Diode based rectifiers are denoted as un-controlled, since they lack any extra input to control voltage or current at the output. SCR rectifiers on the other hand, fall in the category of fully meticulous rectifiers, which are made variable with the help of changing output current's angle.



Figure 8: Block Diagram

Such rectifiers are used in devices where high power, coupled with high voltage is demanded. SCR rectifiers can be further characterized into half wave and full wave rectifiers.

# **Design and calculations**

# VFD

# Explanation

- VCC is a low side voltage that should be between 10v to 20v.
- VDD = 5V as a common ritual, although it can be between 3v to 20v but 5v is adequate for all the circuits since most circuits tend to have 5v outputs.
- As the driver IC i.e. IR2110 is a non-isolated type, VSS and COM both are connected to common ground (don't think of both as independent).
- High input to HIN means a high HO, which means we want to drive high-side MOSFET.HO values should not be considered with respect to ground but VS (high side floating point).



Figure 9: Software design

- The level on HO is equal to that of VB (High side floating point) when high and equal to level of VS when low i.e. zero.
- Similarly, to drive low-side MOSFET, voltage low/high is provided to LIN. Output on LO is always with respect to ground. When LO is high, its level is equal to that of VCC as compared to VSS when LO is low.
- SD pin as the above table specifies, is for shutdown control. A high voltage on pin will disable the drive.

**Hardware Setup:** An H-Bridge is a conventional method to drive a motor. In our case, four MOSFET's are hired to perform such a task. In a H-bridge two MOSFETS are used a high side MOSFETS and other two as low side MOSFET.

# **Design Equations**

(3.1)
(3.2)
(3.3)
(3.4)
(3.5)
or develops
(3.6)
(3.7)
(3.8)
(3.9)
(3.10)
(3.11)
(3.12)
(3.13)

# **SPWM Inverter Design**

# Explanation

- Arduino nano was more preferable in our case as it requires less space.
- Power supply of 5V (input = 12V) is designed for the Arduino's input.
- D2 is to make sure the circuit is working and D1 is for avoiding reverse voltage.

- C1 and C2 further soothes the voltage.
- At J1, transformer would be attached with its centre pin for 12V from the battery.
- Q5 and Q6 will rise the level of Arduino's voltage i.e. 3v to Vin/2 (6V).



Figure 10: Software Design

**Hardware Description:** The hardware side contains two parts i.e. level shifter or switching MOSFETS and a transformer.

# Calculations

(3.14)
(3.15)
(3.16)
(3.17)
(3.18)

# **DC-DC Converters Design**

# Explanation

- P-CHANNEL MOSFET has been used as a power side switch (as it passes good ONE)
- Here BJT at Drain of MOSFET is required to completely turn it off.
- When base voltage of BJT is zero, V(gate) = 12V, So
  Vgs = 0 as Vs = 12V (MOSFET OFF)
- When base voltage of BJT is high, V(gate) = 0V, So
  Vgs =- 12 as Vs = 12V (MOSFET ON)
- $\beta = Ic/Ib$ ,
- where Ib = 3.3-0.7/10K = 0.26mA and
- Ic = β X Ib = 26mA

Here Arduino will take error voltage (due to voltage fluctuations) from output of the Buck Regulator and increase or decrease PWM input to BJT from Arduino in order to adjust the output voltage to its desired value i.e 0-5V (depends on potentiometer adjustment).

**BUCK Converter:** Here we have designed a buck converter with the help of microcontroller board i.e. Arduino.



Figure 11: Software Design

# **Inductor Calculation**

k = duty Cycle	(3.19)
k = Vout/vin	(3.20)
k = 512	(3.21)
k = 0.416	(3.22)
$ton = 1 f \times k$	(3.23)
ton = 1 31.1 × 103 × 0.416	(3.24)
ton = 13ms	(3.25)
Toff = 1 f - ton	(3.26)
$L = (vin - vout) \times Toff / \Delta I$	(3.27)
L = 100mH	(3.28)

**Output Capacitor:** A 33uF, 25V Cap is selected because it has Max ESR of 0.074 and Max ripple current of 2.8A. Max ESR provides good filtering.

**BOOST Converter:** Microcontroller board i.e. Arduino will help us in achieving our goals.



Figure 12: Software Design

#### Calculations

k = duty Cycle	(3.29)
v out = vs 1 – k	(3.30)
k = 1 - vs / v out	(3.31)
k = 1 - 12/30	(3.32)
k = 0.6	(3.33)

$ton = 1/f \times k$	(3.34)
$ton = 1/31.1 \times 10^{3} \times 0.6$	(3.35)
ton = 19us	(3.36)
toff = 1/f - ton	(3.37)
$L = (vi - vo) \times toff / \Delta I$	(3.38)
L = 100uH	(3.39)
C = 33uF, 50V	(3.40)

#### **TRIAC Characteristics Experiment Design**

Following is circuit design for DIAC and TRIAC characteristics experiment.

• Resistance value will decide charging and discharging time of the capacitor.

• DIAC will fire TRIAC when capacitor reaches its break over voltage.

• LAMP is the load and its brightness is dependent upon TRIAC's angle.

• Lesser the angle, more is the brightness and vice versa.



Figure 13: Software Design

**MOTOR Speed Control Using IGBT and MOSFET:** 



Figure 14: Software Design

#### Explanation

- With J2, a potentiometer is attached.
- D1 provides the charging path and D2 helps in providing discharging path.
- C1 decides charging and discharging time.
- 7th pin of 555 will generate PWM.
- J4 is a toggle switch which would change driver IC (IGBT or MOSFET)
- IRG4BC20F and IRFZ44N are used as drivers.

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- With J1, a motor will be attached.
- D3 and D4 will prevent reverse current.
- J5 is where input will be attached.
- With J3 a ON/OFF switch will be involved.
- D5 will be turned ON if MOSFET isturned ON or else D6 when IGBT isinitiated.

#### Calculations

High Time TH =  $0.693 \times R \times C1$ Low Time TL =  $0.693 \times C1$ Period for once cycle T = TH = TL = 0.01386sf = 1/Tf = 72.1Hz

# Circuit Description of Half Wave Controlled Rectifier



Figure 15: Software Design

#### **Explanation**

- R1 and U2 make up a zero cross detector, whose output is given to Arduino's interrupt pin. 55 Modeling and Simulations
- Pin 8 of Arduino will generate a pulse in accordance with positive or negative edge of input (220V) sinusoidal wave and delay generated by the potentiometer.
- U1 (SCR) will receive the pulse and allow portion of input wave to be given to the load (depends upon delay).

# Calculations

$Vmax = \sqrt{2Vs}$	(3.41)
$Vmax = \sqrt{2 \times 220} = 314V$	(3.42)
$Io(avg) = Vmax(1 + cos a) 2\pi R$	(3.43)
$a = 0 \rightarrow 180 \circ$	(3.44)
R = 100∩	(3.45)
Irms = $(Im/2) [1 + a/\pi + sin 2a/2\pi]^{0.5}$	(3.46)
$Vo = Vm \pi (at \rightarrow a = 0 \circ )$	(3.47)
Vn = Vo Vdo	(3.48)
$Vn = (1 + \cos a) 2 (0 \le a \le 180 \circ)$	(3.49)

#### **Full wave Controlled Rectifier**

![](_page_5_Figure_19.jpeg)

#### Figure 16: Software Design

#### Calculations

$Vmax = \sqrt{2Vs}$	(3.50)
$Vmax = \sqrt{2 \times 220} = 314V$	(3.51)
Vavg = Vmax/ (1 + cos a)	(3.52)
$a = 0 \rightarrow 180 \circ$	(3.53)
Vavg = Vmax/ RL(1 + cos a)	) (3.54)
R = 100∩	(3.55)

# **Results and Discussions**

Following are the results and their discussions for each particular design. Due to the pandemic, we were left with only two circuits for hardware (light dimmer and SCR circuits) and the final body for the trainer. The hardware calculations and reading were yet to be determined in fully detail but were left due to the pandemic but are fully working and videos have been made as a proof.

#### **Single phase VFD**

Single phase VFD is designed for a frequency of 50 Hz to 120 Hz. This can be changed through the code. Following are the results.

![](_page_5_Figure_27.jpeg)

Figure 17: Waveform output from VFD

# **PCB Design**

The four circles at the corners of the board are for the board's attachment to the trainer board. Some connections could not be made during design, so were left as shown in red lines. They were made after the hardware was made.

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Figure 18: PCB Design

#### **SPWM Inverter**

The inverter designed was tested on hardware and show adequate results for a load of 10W with 201 Volt AC max at the output. Following are the simulation results.

# Waveform At output

Both waveform are at transformers output terminals.

![](_page_6_Picture_7.jpeg)

Figure 19: Waveform

These waveforms are generated through Arduino for MOSFET gates.

![](_page_6_Figure_10.jpeg)

Figure 20: SPWM Pulses

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# **PCB Design**

![](_page_6_Figure_14.jpeg)

Figure 21: PCB Design

# **DC-DC Converters**

# **Buck Converter**

Design has been explained in chapter. We can witness 11.7 V at output in case of 85%.

# **Virtual Terminal**

Here V req is the voltage we want at the output and can be varied to 5V through potentiometer. At 95 percent duty cycle V out is 12.67 volt. Due to the consequent switching, the voltage lacks stabilization but no such behaviour was seen in the hardware testing.

Virtual Terminal			
V_in 12.73	V_out 12.67	V_req 12.00	Duty 95
V_in 12.73	V_out 12.67	<b>V_req 12.00</b>	Duty 94
V_in 12.73	V_out 12.67	V_req 12.00	Duty 93
V_in 12.73	V_out 12.67	<b>V_req 12.00</b>	Duty 92
V_in 12.73	V_out 12.67	<b>V_req 12.00</b>	Duty 91
V_in 12.73	V_out 12.67	<b>V_req 12.00</b>	Duty 90
V_in 12.73	V_out 12.67	<b>V_req 12.00</b>	Duty 89

Figure 22: Virtual Window

#### **Duty Cycle Waveform of Buck Converter**

The following wave form shows 35 percent duty cycle PWM wave given to MOSFET gate.

![](_page_6_Figure_25.jpeg)

Figure 23: Duty cycle Waveform

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# **PCB Design**

![](_page_7_Picture_3.jpeg)

Figure 24: PCB Design

# **Boost Converter**

**Virtual Terminal** and **Waveform** of boost Regulator Here V out is not the output voltage but the voltage across a voltage divider at the output. V req is the output voltage respectively.

![](_page_7_Figure_7.jpeg)

V in :10.0	5
<b>V</b> Out :9.68	
<b>V</b> req :29.0	9
duty :83	
V in :10.0	5
<b>v</b> out :9.84	
<b>V</b> req :29.0	9
duty :84	
V in :10.0	5
V Out :	

Figure 25: Virtual screen

# PCB Design

![](_page_7_Figure_11.jpeg)

Figure 26: PCB Design

# **Controlled Rectifiers**

The hardware of the controlled rectifiers could not be designed on hardware due to the pandemic but basic simulation and idea is complete for the hardware implementation.

# Waveform of Half wave

These are two waveforms taken at different angles. The first one shows output waveform at alpha – 45 degree. The blue wave shows the out.

![](_page_7_Figure_17.jpeg)

Figure 23

![](_page_7_Figure_19.jpeg)

Figure 27: PCB Design

# Waveform of Full wave

**PCB Design:** 

Following Waveform is not accurate due to some design problems; we are trying our best for the better result and still working on its design.

![](_page_7_Figure_23.jpeg)

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#### **PCB Design**

![](_page_8_Figure_3.jpeg)

Figure 29

**MOTOR Speed Control Through MOSFET and IGBT: PWM Waveform:** Here are two forms at different duty cycles.

![](_page_8_Figure_6.jpeg)

![](_page_8_Figure_7.jpeg)

Digital Oscilloscope

**PCB Design** 

![](_page_8_Figure_9.jpeg)

![](_page_8_Figure_10.jpeg)

![](_page_8_Picture_11.jpeg)

Figure 32

![](_page_8_Figure_13.jpeg)

![](_page_8_Figure_14.jpeg)

![](_page_8_Figure_15.jpeg)

PCB Design

![](_page_8_Figure_17.jpeg)

Figure 34

# Conclusion

The trainer we designed is more efficient.It consist of the basic circuitry of power electronics and Microcontroller based applications. All circuits are provided with fix voltages internally, will be controlled by switches just. The design is easy to carry and selfexplanatory for the group of three people. This trainer is basically easy to handle version for the lab engineers and make their teachings more easier with the help of briefed designed present in the trainer. Moreover the Micro-controller used in some circuitry is controlling the angle delays of the SCR. Other then this we are generating pluses for spwm inverter, buck and boost circuitry. The designed is managed in such a way that we can get update to it.Micro-controller used in trainer was Arduino-NANO which was more compatible with all circuits and was cheap. For the low price trainer we find it the best Micro-controller in the market. Trainer consist of Vfd, Buck, Boost, SPWM inverter, Controlled Rectifiers, MOTOR Speed Characteristics using IGBTS and MOSFETS. These circuits are the pillars of power electronics and so we highlighted them in our trainer so it should be the best source of learning for the future engineers.

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